


<b>Search Notes</b> 	<b>Application/Control No.</b> 10581873	<b>Applicant(s)/Patent Under Reexamination</b> NIGGEMEIER ET AL.
	<b>Examiner</b> Mark A Giardino	<b>Art Unit</b> 4113

SEARCHED			
Class	Subclass	Date	Examiner
711	158	11/7/2007	MAG
711	151	2/25/2009	MAG
711	105	2/25/2009	MAG

SEARCH NOTES		
Search Notes	Date	Examiner
EAST - see attached	11/7/2007	MAG
Google search of "combine ram chip enable"	11/7/2007	MAG
Consulted International Search Report	11/7/2007	MAG
Update search	2/25/2009	MAG
Update search	8/4/2009	MAG
Google patents search for "activate precharge priority"	2/18/2010	MAG
Update search	2/19/2010	MAG
Update search	10/8/2010	MAG
Google search for "prioritize memory commands" and "memory bus"	10/8/2010	MAG
Consulted with Stephen Elmore	10/19/2010	MAG

INTERFERENCE SEARCH			
Class	Subclass	Date	Examiner
711	158	10/20/2010	MAG
711	151	10/20/2010	MAG
711	105	10/20/2010	MAG

--	--